

BR25G512FJ-3GE2






Part Number: [BR25G512FJ-3GE2](#)

Manufacturer: [LAPIS Semiconductor](#)

Description: SPI BUS EEPROM

Data sheet:  [SOP-J8 Taping Spec](#)
 [BR25G512\(F,FJ,FVT\)-3](#)

RoHs Status: Lead free / RoHS Compliant

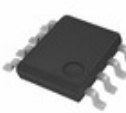
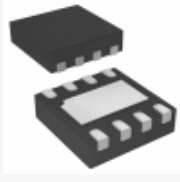
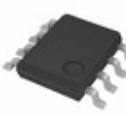
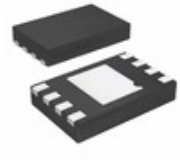
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PRODUCT PARAMETER			
Part Number	BR25G512FJ-3GE2	Manufacturer	LAPIS Semiconductor
Description	SPI BUS EEPROM	Lead Free Status / RoHS Status	Lead free / RoHS Compliant
Quantity Available	114922 pcs	Data sheet	SOP-J8 Taping Spec BR25G512(F,FJ,FVT)-3
Category	Integrated Circuits (ICs)	Write Cycle Time - Word, Page	5ms
Voltage - Supply	1.8 V ~ 5.5 V	Technology	EEPROM
Supplier Device Package	8-SOP-J	Series	-
Packaging	Tape & Reel (TR)	Package / Case	8-SOIC (0.154", 3.90mm Width)
Other Names	BR25G512FJ-3GE2TR	Operating Temperature	-40°C ~ 85°C (TA)
Mounting Type	Surface Mount	Moisture Sensitivity Level (MSL)	1 (Unlimited)
Memory Type	Non-Volatile	Memory Size	512Kb (64K x 8)
Memory Interface	SPI	Memory Format	EEPROM
Manufacturer Standard Lead Time	9 Weeks	Lead Free Status / RoHS Status	Lead free / RoHS Compliant
Detailed Description	EEPROM Memory IC 512Kb (64K x 8) SPI 10MHz 8-SOP-J	Clock Frequency	10MHz
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